

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC - AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution
www.mits.ac.in

Examination Section

Schedule for B.Tech – I Year I Semester (R14) Regular End Semester Examinations, December 2016

Academic Year – 2016 - 17 (For 2016 Admitted Batch Only)

TIME TABLE

Timings: FN: 02:00 PM to 05:00 PM

			TIME TABLE		Timings. Fiv. 02.00 Five to 03.00	
DATE / DAY	Civil Engineering (CE)	Electrical & Electronics Engineering (EEE)	Mechanical Engineering (ME)	Electronics & Communication Engineering (ECE)	Computer Science & Engineering (CSE)	
14.12.2016 Wednesday	******			Engineering Chemistry 14CHE11T01 (Common to ECE & CSE)		
16.12.2016 Friday	Engineering Physics 14PHY12T01 (Common to CE,EEE & ME)			Entonomi		
19.12.2016 Monday	Advanced Calculus - 14MAT11T01 (Common to All)					
21.12.2016 Wednesday	Functional English - 14ENG11T01 (Common to All)					
23.12.2016 Friday	Environmental Science 14CHE11T02 (Common to All)					
26.12.2016 Monday	Basic Electrical & Electronics Engineering 14EEE12T01 (Common to CE,EEE & ME)					
		**Engineering Graphics	Time Table is displayed in	page 02 of 02		

Note: (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.

(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

CONTROLLER OF EXAMINATIONS

Date: 29.11.2016

Copy to: P.A to Principal

All the HODs for circulation among the staff members and display on notice board,

Assistant Controllers of Examinations,

Senior Administrative Officer,

Placement Officer. Transport incharge PRINCIPAL

PRINCIPAL

Madanapalle Institute of Technology & Science
P.O. No: 14, Kadiri Road, Angallu

MADANAPALLE - 517 325, A.P.



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC - AUTONOMOUS)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)
An ISO 9001: 2008 Certified Institution
www.mits ac.in

Examination Section

Schedule for B.Tech – I Year I Semester (R14) Regular End Semester Examinations, December 2016
Academic Year – 2016 - 17 (For 2016 Admitted Batch Only)

TIME TABLE for Engineering Graphics - 14ME11T01 Timings: FN: 02:00 PM to 05:00 PM

DATE / DAY	Electronics & Communication Engineering (ECE)	Computer Science & Engineering (CSE)	
26.12.2016 Monday		16691A0501 - 16691A05H0	
27.12.2016 Tuesday	16699A04A1 – 16699A04C0	16691A05H1 - 16691A05H8, 16699A0501 - 16699A0559	
28.12.2016 Wednesday	16691A0401 – 16691A04H0		
29.12.2016 Thursday	16691A04H1 – 16691A04O0, 16699A0401 – 16699A04A0		

Note: (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.

(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

CONTROLLER OF EXAMINATIONS

Date: 29.11.2016

Copy to: P.A to Principal

All the HODs for circulation among the staff members and display on notice board,

Assistant Controllers of Examinations,

Senior Administrative Officer,

Placement Officer. Transport incharge PRINCIPAL
PRINCIPAL
Madanapalle Institute of Technology & Science

P.O. No: 14, Kadiri Road, Angaliu MADANAPALLE - 517 325, A.P.